

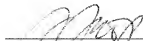
REMARKS

The claims have been rejected based on the reference to Beard. Beard was filed on May 17, 2002, which is later than the present application. Beard is based on a provisional application filed on May 17, 2001, which could make it prior art. However, a copy of that provisional application is attached and it does not provide support for the features relied upon in the office action.

Therefore, the Beard issued patent is not effective prior art against the present application and, therefore, reconsideration is respectfully requested.

Respectfully submitted,

Date: July 27, 2010



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PROVISIONAL APPLICATION COVER SHEET

This is a request for filing a PROVISIONAL APPLICATION under 37 C.F.R. §1.53(b)(2).

Docket Number	CD 01-006B	Type a plus sign (+) inside this box **	+
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INVENTOR(s)/APPLICANT(s)

LAST NAME	FIRST NAME	MIDDLE INITIAL	RESIDENCE (CITY AND EITHER STATE OR FOREIGN COUNTRY)
Beard	Paul		1657 Country Club Drive Milpitas, CA 95035 Citizen of the United Kingdom

☐ Additional inventors are being named on the ___ separately numbered sheets attached hereto

TITLE OF THE INVENTION (280 characters max)

DUAL PROCESSOR FRAMER

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ENCLOSED APPLICATION PARTS (check all that apply)

<input checked="" type="checkbox"/>	Specification	Number of Pages	2
<input checked="" type="checkbox"/>	Drawing(s)	Number of Sheets	1

METHOD OF PAYMENT

The Commissioner is hereby authorized to charge filing fees and credit Deposit Account Number: 19-4882	<input type="checkbox"/> Applicant claims Small Entity Status. See 37 CFR 1.27.	Provisional Filing Fee Amount: \$150.00

The invention was made by an agency of the United States Government or under a contract with an agency of the United States Government

☒ No.

☐ Yes, the name of the U.S. Government agency and the Government contract number are: _____

Respectfully submitted,



Sean Patrick Suiter, Reg. No. 34,260



23531

PATENT TRADEMARK OFFICE

May 17, 2001

CERTIFICATE OF MAILING

I hereby certify that this *Provisional Patent Application Cover Sheet* and *Provisional Patent Application* are being deposited with the United States Postal Service on this date, May 17, 2001, in an envelope as "Express Mail Post Office to Addressee", Mailing Label No. EL 839 818 251 US addressed to: Box Provisional Patent Application, Assistant Commissioner for Patents, US PTO, Washington, DC 20231.



Sean Patrick Suiter

DATED: May 17, 2001

DUAL PROCESSOR FRAMER

In an exemplary embodiment of the present invention, a dual processor framer is provided. Although a dual processor framer is described, it should be apparent that framer more than two processors are contemplated by the present invention without departing from the spirit and scope thereof.

In a first aspect of the present invention, the dual processor framer supports asynchronous serial communications wherein units of transmission, which may be measured in elapsed time such as beginning with a start bit that precedes a character and ends with a last stop bit that follows a character, and synchronous communications are provided, in which a package of information transmitted as a single unit, such as having every frame following similar basic organization and containing control information, such as synchronizing characters, station address, error-checking values, variable amounts of data, and the like as contemplated by a person of ordinary skill in the art. For example, a dual processor framer may be provided in which the first processor provides asynchronous communication and the second processor provides synchronous communication.

In a second aspect of the present invention, a dual processor framer is suitable for providing simultaneous communication between multiple channels, such as multiple channel communication transmittal, multiple channel receipt, simultaneous receipt and transmittal, and the like as contemplated by a person of ordinary skill in the art without departing from the spirit and scope of the present invention. For example, one processor may operate on a channel different from the second processor, may alternate hops performed between processors, frame data from two different sources, transmit utilizing a first processor and receive utilizing a second processor, and the like.

In a third aspect of the present invention, a dual processor framer is provided which includes a first processor suitable for operation at a first power level and a second processor suitable for operation at a second power level, the first power level may be a power saving level, such as utilizing less power than the second power level. For example, the framer may be utilized to provide multiple transmission based on power requirements, both of the power requirements of the device utilizing the framer, such as

in a power savings mode, and the requirements of transmittal, such as the utilization of increased power to increase range of the transmitter.

It should be apparent that aspects of the invention may be combined and/or separated, the previous discussion being merely exemplary. Attached hereto is an exemplary dual processor framer in accordance with an embodiment of the present invention.

